

P2131\_A00

GK208 GDDR5 128X32Bit  
DP+DP

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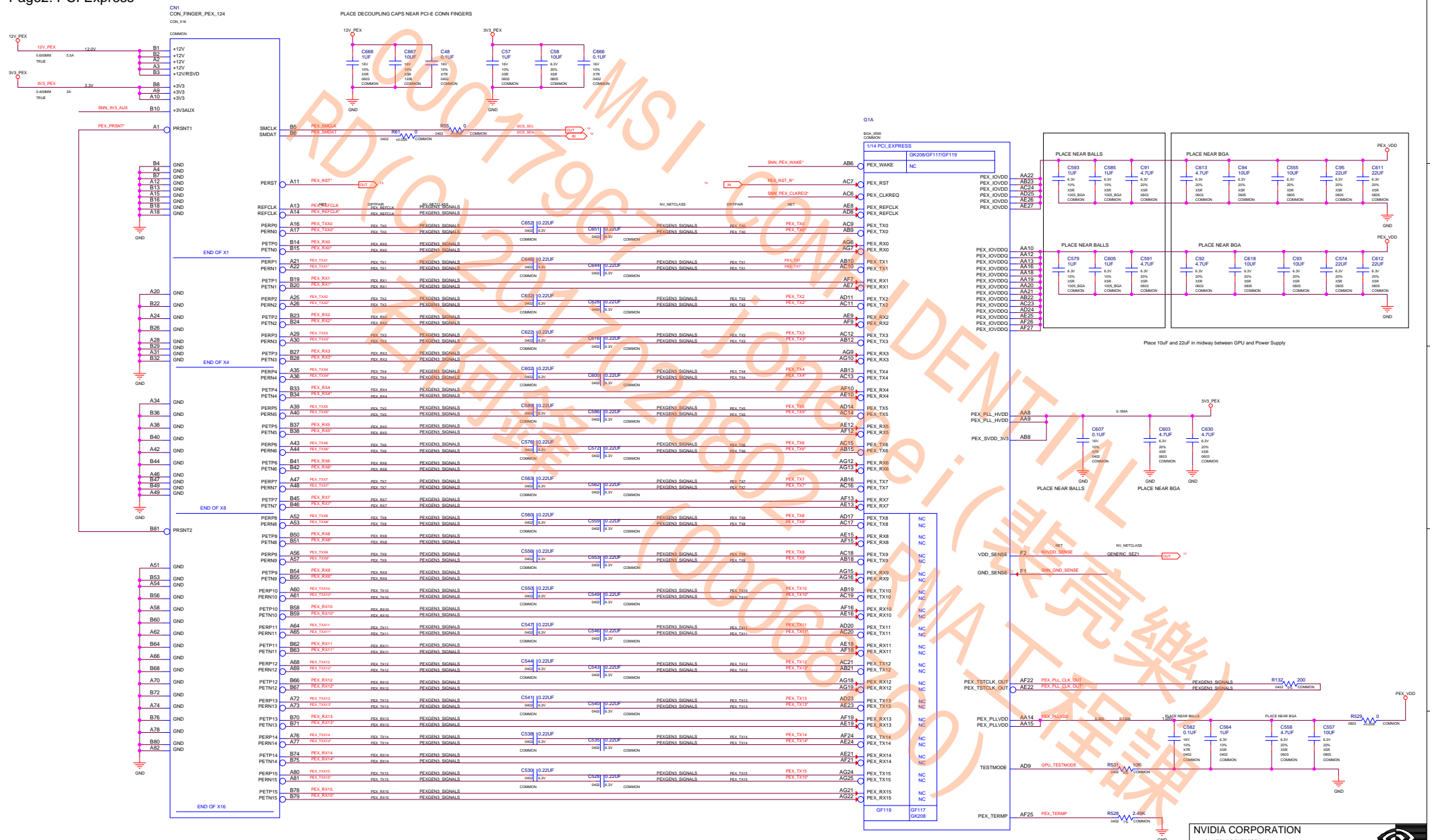
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PAGE DETAIL	PCI EXPRESS

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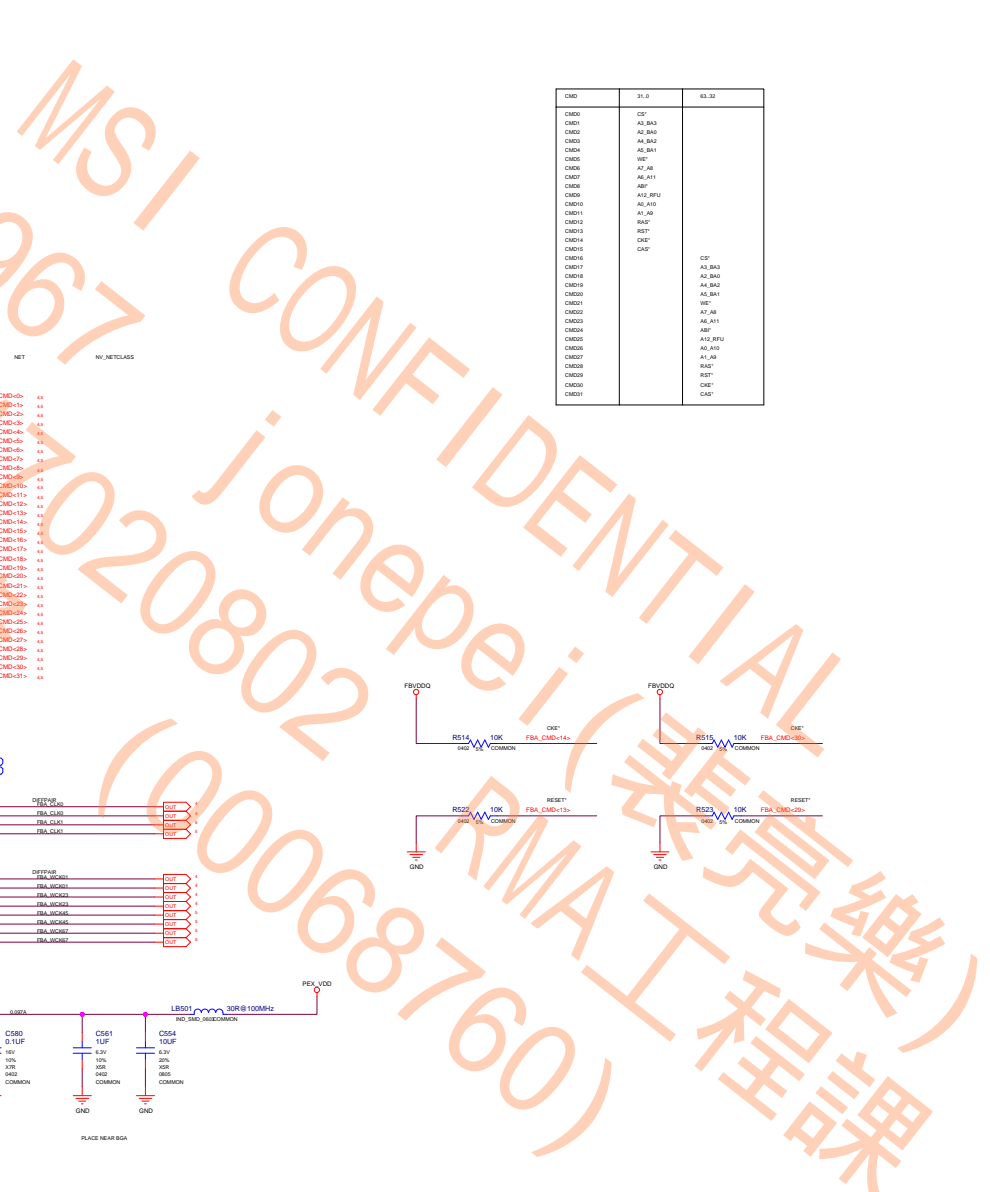
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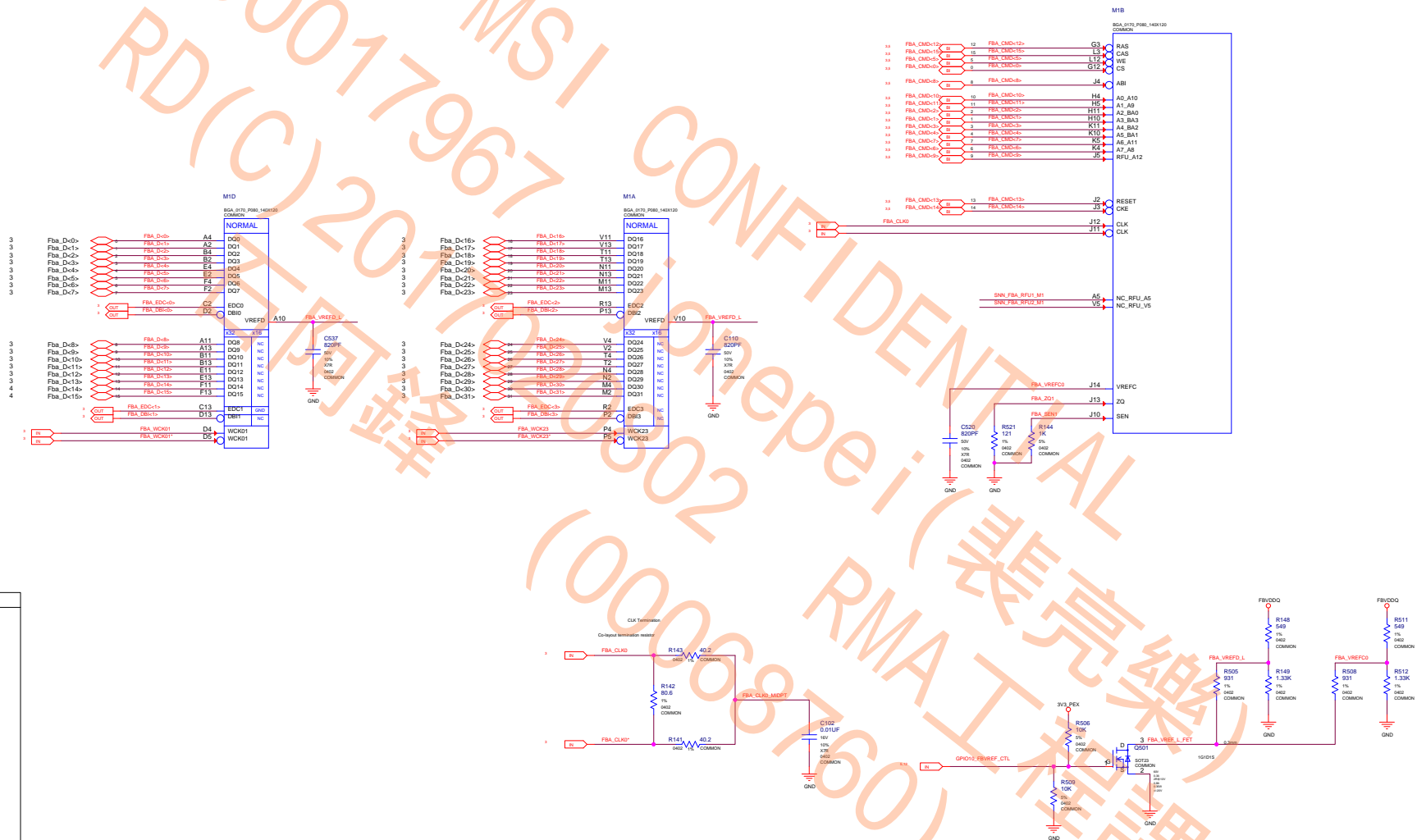


CMO	31.0	33.35
CM01	CS <sup>2</sup>	
CM02	A3_BA3	
CM03	A2_BA0	
CM04	A4_BA2	
CM04	A4_BA1	
CM05	W0 <sup>2</sup>	
CM06	A17_A4	
CM07	A6_A11	
CM08	AB <sup>2</sup>	
CM09	A12_RFU	
CM10	A4_A10	
CM11	A1_A8	
CM12	RA5 <sup>2</sup>	
CM13	RB1 <sup>2</sup>	
CM14	CH1 <sup>2</sup>	
CM15	CA5 <sup>2</sup>	
CM16		CS <sup>2</sup>
CM17	A3_BA3	
CM18	A2_BA0	
CM19	A4_BA2	
CM20	A5_BA1	
CM21	W0 <sup>2</sup>	
CM22	A17_A4	
CM23	A6_A11	
CM24	AB <sup>2</sup>	
CM25	A12_RFU	
CM26	A4_A10	
CM27	A1_A8	
CM28	RA5 <sup>2</sup>	
CM29	RB1 <sup>2</sup>	
CM30	CH1 <sup>2</sup>	
CM31	CA5 <sup>2</sup>	

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CM5	31-8	63-30
CM00	CS*	
CM01	A3, BA3	
CM02	A2, BA2	
CM03	A4, BA2	
CM04	A6, BA1	
CM05	WE*	
CM06	A7, A8	
CM07	A6, A11	
CM08	AB*	
CM09	A12, SFU	
CM10	A6, A10	
CM11	A1, A8	
CM12	BA2*	
CM13	B2*	
CM14	CB*	
CM15	CA*	
CM16		CB*
CM17		A3, BA3
CM18		A2, BA2
CM19		A4, BA2
CM20		A6, BA1
CM21		WE*
CM22		A7, A8
CM23		A6, A11
CM24		AB*
CM25		A12, SFU
CM26		A6, A10
CM27		A1, A9
CM28		BA2*
CM29		B2*
CM30		CB*
CM31		CA*







DECOUPLING FOR M1 and M501 DEVICE

DECOUPLING FOR M2 and M502 DEVICE

PLACE 1uF CAPS CLOSEST TO THE MEMORY DEVICES

PLACE LARGER CAPACITORS SLIGHTLY FARTHER AWAY

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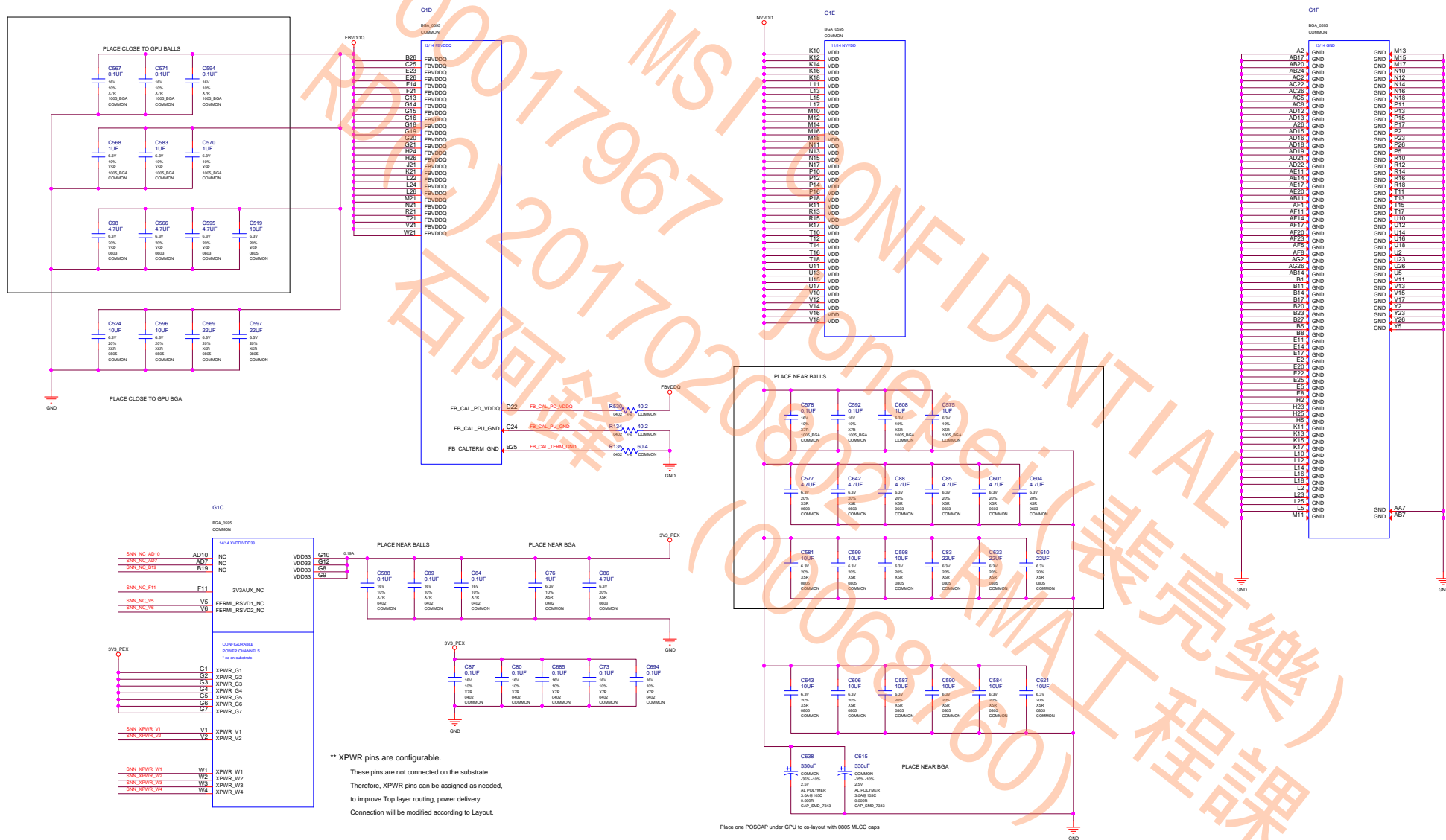
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PAGE DETAIL	POWER/DECOUPLING: NVDD, FBVDDQ, VDD33, GND

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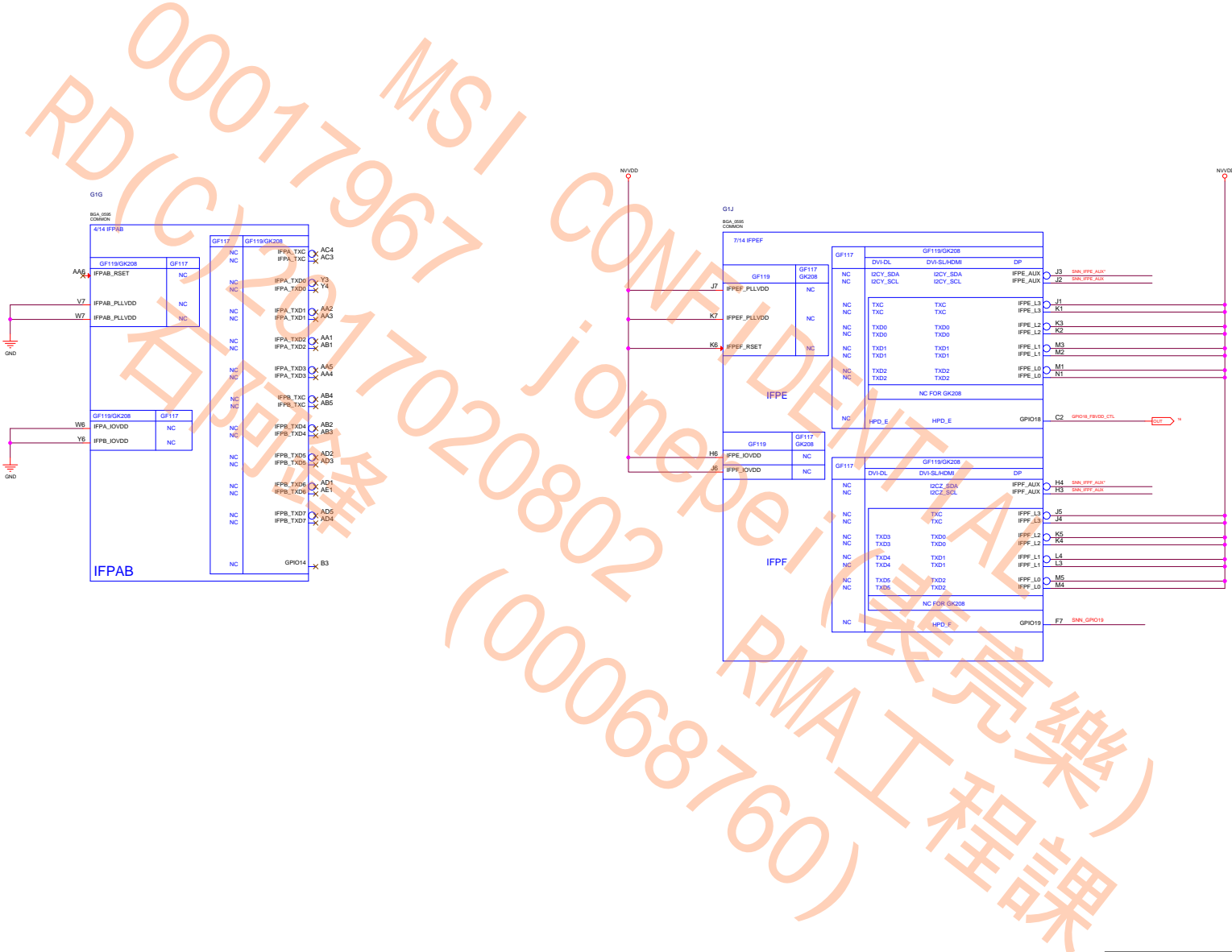
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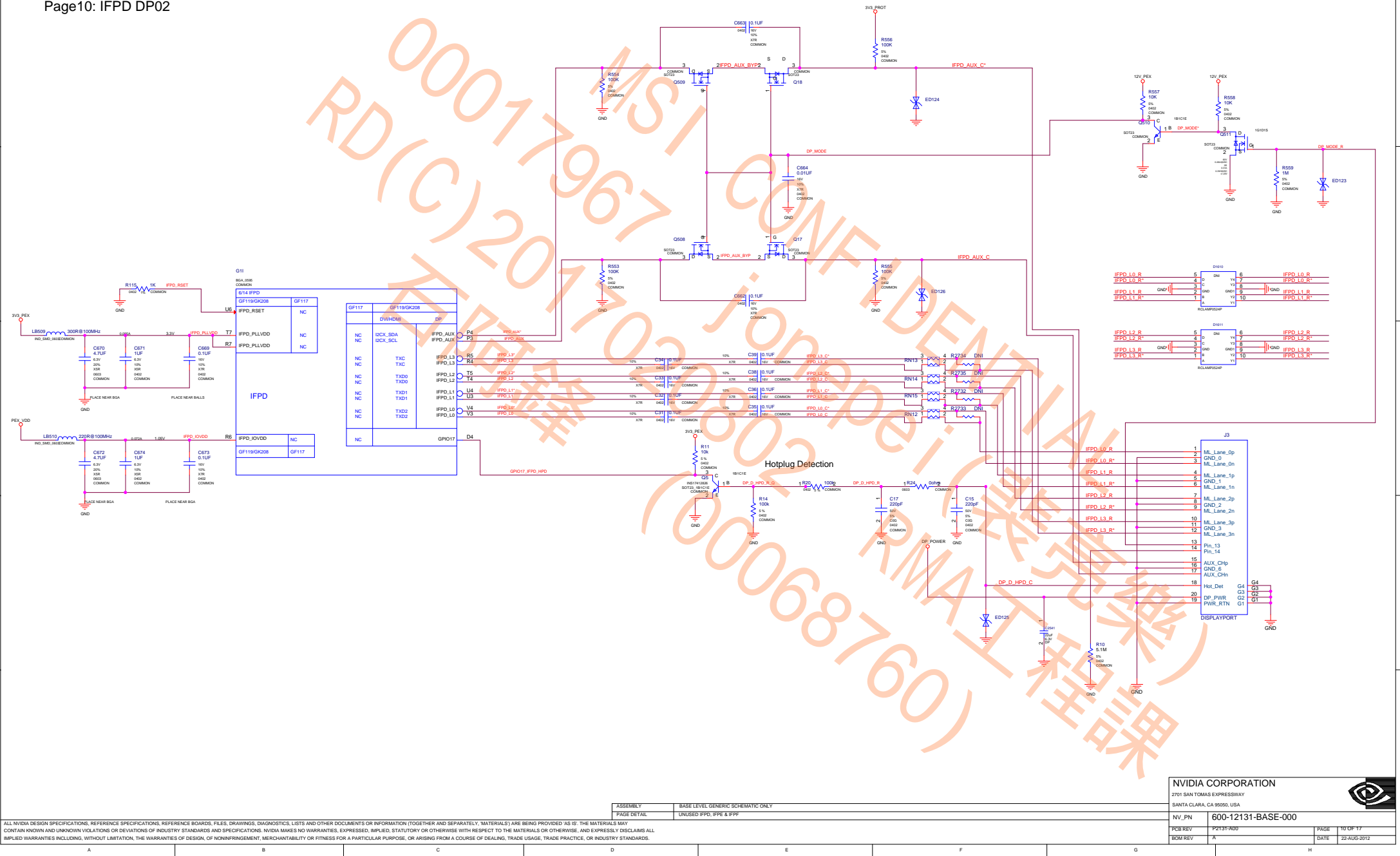
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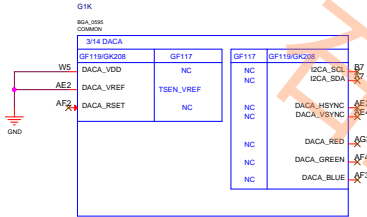




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GK208 GB2-64 package only supports  
DACA, so one SKU can only have  
one VGA output: North or South.

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Multilevel Straps

0000	5.1K to GND	1000	5.1K to VCC
0001	10K to GND	1001	10K to VCC
0010	15K to GND	1010	15K to VCC
0011	20K to GND	1011	20K to VCC
0100	24.9K to GND	1100	24.9K to VCC
0101	30.1K to GND	1101	30.1K to VCC
0110	34.8K to GND	1110	34.8K to VCC
0111	45.3K to GND	1111	45.3K to VCC

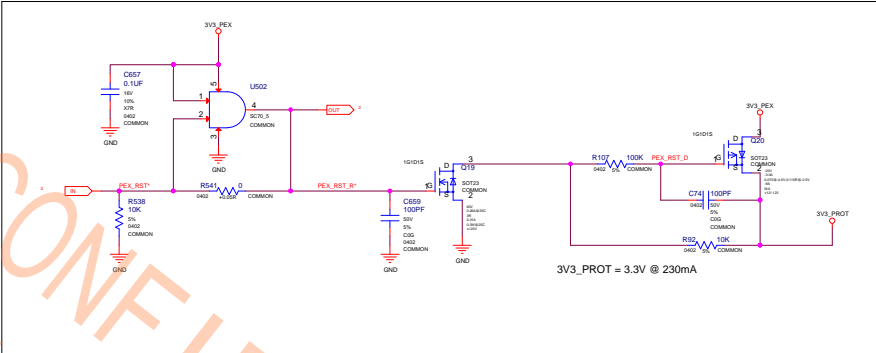
GK208 Straps

Bit Signal

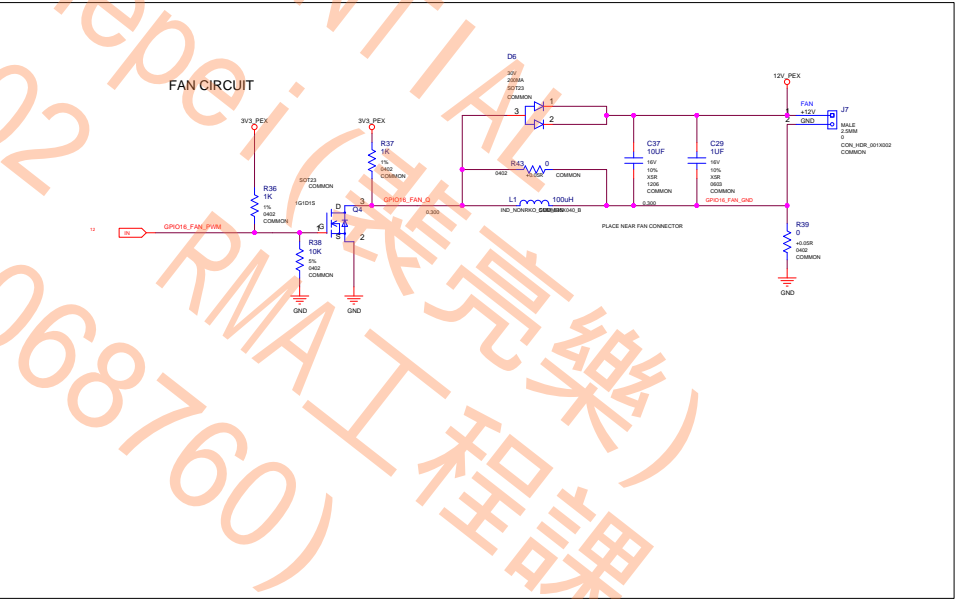
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03	RAMCFG[0]	0000	Reserved			ROM_SI
02	RAMCFG[0]	0001	1GBA-V6 GCORE3 X-V6 64MB EDRAM			
01	RAMCFG[0]	0010	1GBA-V6 GCORE3 X-V6 64MB Physica Alloc			
00	RAMCFG[0]	0011	1GBA-V6 GCORE3 X-V6 64MB Streaming D-Ram			
03	FB[0]	01	128MB			ROM_SO
02	FB[0]	10	256MB			
01	SMBL_ALT_ADDR	0	DISABLED	1	ENABLED	
00	VGA_DEVICE	0	DISABLED	1	ENABLED	
03	PCI_DEV0_EXT	0000	0	SHUT	TBD	ROM_SCLK
02	SUB_VENDOR	0	NO BIOS	1	BIOS	
01	PCI_DEV0_EXT_RTS	0000	0	SHUT	TBD	
00	PEX_FAL_EN_TERMINO	0	DISABLED	1	ENABLED	
03	USER[0]	0000	NOT			Strap0
02	USER[0]					
01	USER[0]					
00	USER[0]					
03	3G0G_PADCFG_LUT_ADDR[0]	0000	DISKTOP_DEFAULT	1000	DISKTOP_JTHRS	Strap1
02	3G0G_PADCFG_LUT_ADDR[0]	0001	MOBILE_DEFAULT	1001	MOBILE_JTHRS_SHAMP	
01	3G0G_PADCFG_LUT_ADDR[0]	0010	MOBILE_JTHRS_LLAMP	1010	MOBILE_JTHRS_LLAMP	
00	3G0G_PADCFG_LUT_ADDR[0]	0011	MOBILE_JTHRS_LAMP	1011	MOBILE_JTHRS_LAMP	
03	3G0G_PADCFG_LUT_ADDR[0]	0100	MOBILE_JTHRS_LAMP	1100	MOBILE_JTHRS_LAMP	
02	3G0G_PADCFG_LUT_ADDR[0]	0101	MOBILE_JTHRS_SHAMP	1101	MOBILE_JTHRS_SHAMP	
01	3G0G_PADCFG_LUT_ADDR[0]	0110	MOBILE_JTHRS_SHAMP	1110	MOBILE_JTHRS_SHAMP	
00	3G0G_PADCFG_LUT_ADDR[0]	0111	MOBILE_JTHRS_SHAMP	1111	MOBILE_JTHRS_SHAMP	
03	PCI_DEV0[0]	0000	0010			Strap2
02	PCI_DEV0[0]					
01	PCI_DEV0[0]					
00	PCI_DEV0[0]	SHUT	TBD			
03	SC0L_EXP0SD	0	DISABLED	1	ENABLED	Strap3
02	SC0L_EXP0SD	0	DISABLED	1	ENABLED	
01	SC0L_EXP0SD	0	DISABLED	1	ENABLED	
00	SC0L_EXP0SD	0	DISABLED	1	ENABLED	
03	PCI_SPEED_CHANGE_GEN0	0	DISABLED	1	ENABLED	Strap4
02	PCI_SPEED_CHANGE_GEN0	0	DISABLED	1	ENABLED	
01	PCI_SPEED_CHANGE_GEN0	0	GEN1	1	GEN2	
00	DP_FAL_VDD_3V3	0	USE V6	1	USE V3V3	

PCI-E Reset



FAN CIRCUIT



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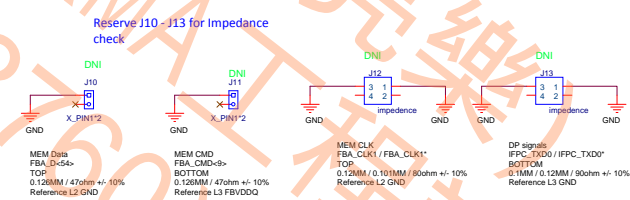
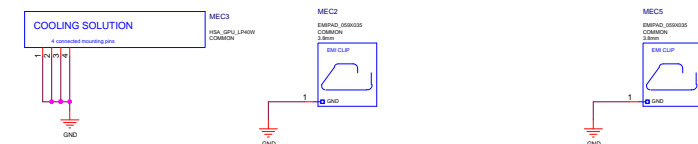
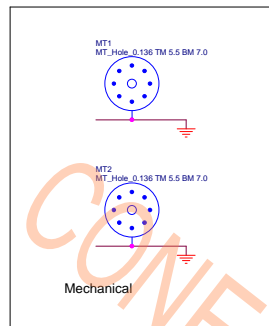
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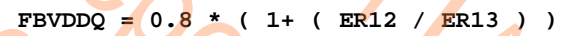
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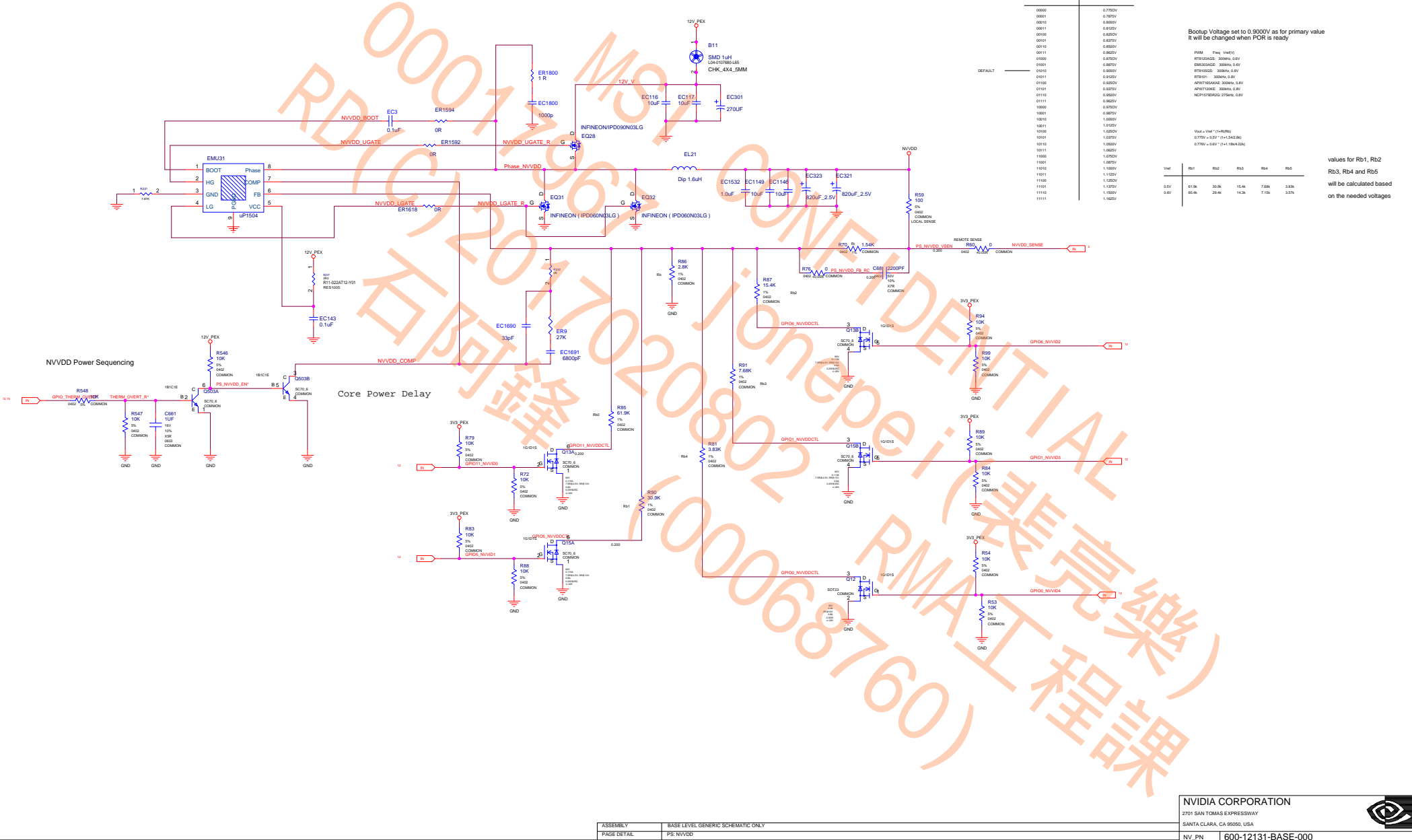
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